Hewlett Packard Enterprise



# Portable Support for GPUs and Distributed-Memory Parallelism in Chapel

Andrew Stone, Engin Kayraklioglu May 9, 2024

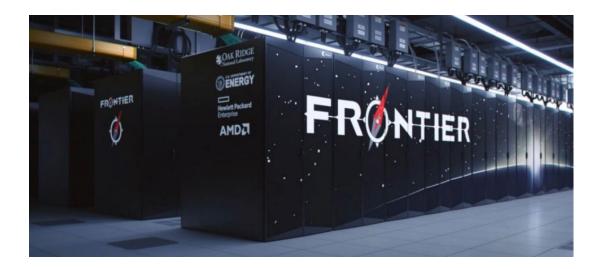
## It is Hard to Avoid GPUs in HPC

#### In the top500 list:

- From June 2011 Nov 2023 there has been a 13x increase in the number of supercomputers with GPUs
- Over the past three years 72% of systems in the top 10 had GPUs

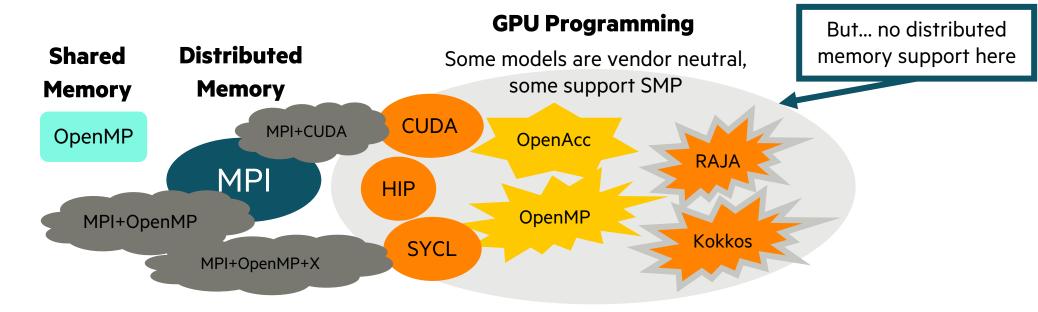
#### For the #1 system today (Frontier):

• 95% of its compute capability comes from its GPUs



#### GPUs are Easy to Find...

# **BUT DIFFICULT TO PROGRAM**



- Programming for multiple nodes with GPUs appears to require at least 2 programming models
  - all of the models rely on C/C++/Fortran, which are less commonly taught these days
  - as a result, using GPUs in HPC has a high barrier of entry

Chapel is an alternative for productive distributed/shared memory GPU programming in a vendor-neutral way.

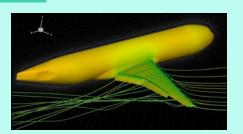
## What is Chapel?

# Chapel: A modern parallel programming language

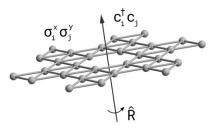
- its goal is to make parallel programming at scale far more productive
- open-source & collaborative
- portable & scalable
  - works on everything from your laptop to a supercomputer
  - Linux laptops/clusters, Cray systems, MacOS, WSL, AWS, Raspberry Pi
  - shown to scale on Cray networks (Slingshot, Aries), InfiniBand, RDMA-Ethernet
  - NVIDIA and AMD GPUs



#### **Applications of Chapel**

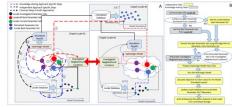


**CHAMPS: 3D Unstructured CFD** Laurendeau, Bourgault-Côté, Parenteau, Plante, et al. École Polytechnique Montréal

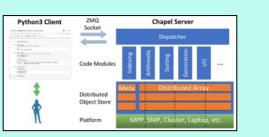


Lattice-Symmetries: a Quantum Many-Body Toolbox Desk dot chpl: Utilities for Environmental Eng.

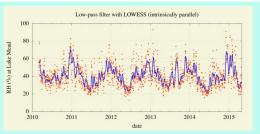
Tom Westerhout Radboud University



**Chapel-based Hydrological Model Calibration** Marjan Asgari et al. University of Guelph



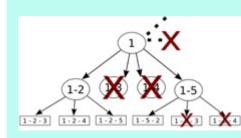
Arkouda: Interactive Data Science at Massive Scale Mike Merrill, Bill Reus, et al. U.S. DoD



Nelson Luis Dias The Federal University of Paraná, Brazil



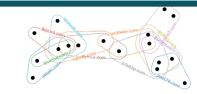
CrayAl HyperParameter Optimization (HPO) Ben Albrecht et al. Cray Inc. / HPE



**ChOp: Chapel-based Optimization** T. Carneiro, G. Helbecque, N. Melab, et al. INRIA, IMEC, et al.

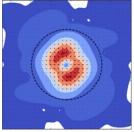


**RapidQ: Mapping Coral Biodiversity** Rebecca Green, Helen Fox, Scott Bachman, et al. The Coral Reef Alliance

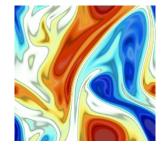


**CHGL: Chapel Hypergraph Library** Louis Jenkins, Cliff Joslyn, Jesun Firoz, et al. PNNL

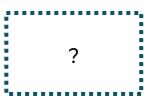
**Active GPU efforts** 



**ChplUltra: Simulating Ultralight Dark Matter** Nikhil Padmanabhan, J. Luna Zagorac, et al. Yale University et al.



**ChapQG: Layered Quasigeostrophic CFD** Ian Grooms and Scott Bachman University of Colorado, Boulder et al.



Your Application Here?

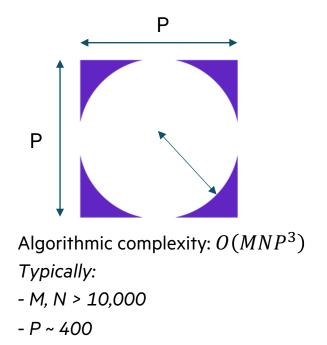
# Use Case: Coral Reef Code

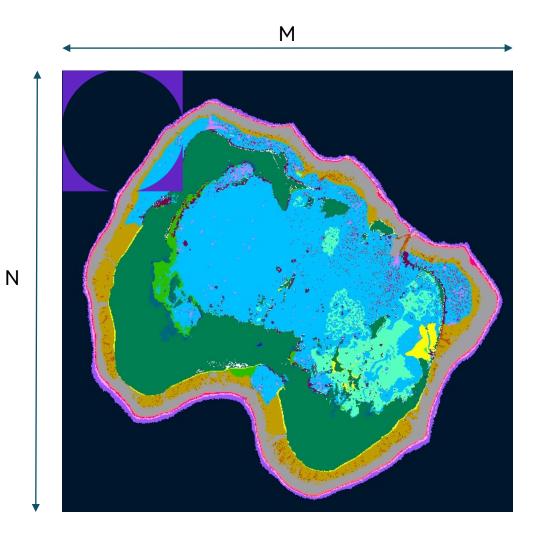
# **Coral Reef Spectral Biodiversity**

1. Read in a (M  $\times$  N) raster image of habitat data

2. Create a (P x P) mask to find all points within a given radius.

3. Convolve this mask over the entire domain and perform a weighted reduce at each location.





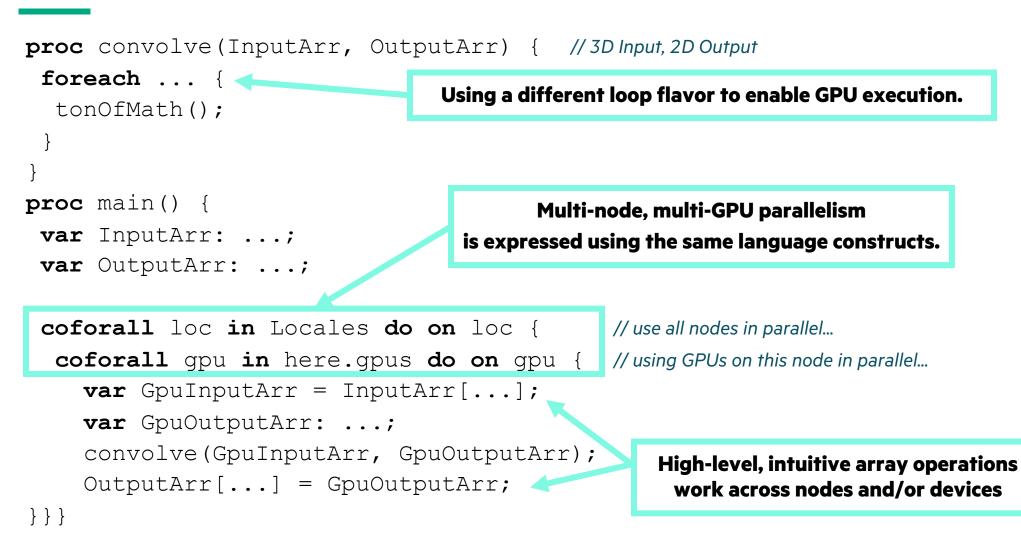
For more info see: "High-Performance Programming and Execution of a Coral Biodiversity Mapping Algorithm Using Chapel" by Scott Bachman et al. CHIUW 2023

#### **Coral Reef Spectral Biodiversity**

```
proc convolve(InputArr, OutputArr) { // 3D Input, 2D Output
for ... {
   tonOfMath();
  }
}
proc main() {
  var InputArr: ...;
  var OutputArr: ...;
```

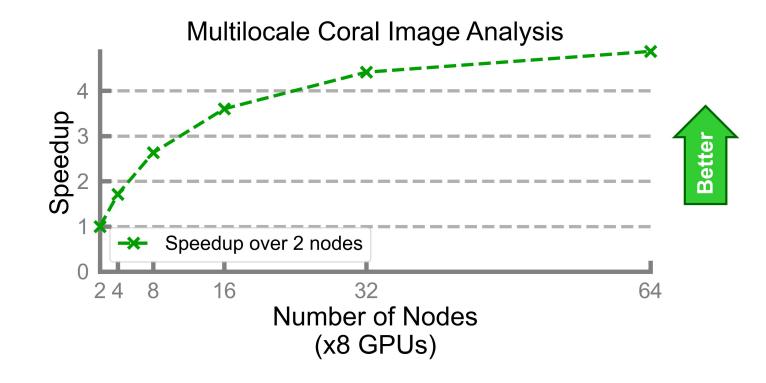
```
convolve(InputArr, OutputArr);
```

#### **Coral Reef Spectral Biodiversity**



## **Coral Reef Takeaway Points**

- Runs on multiple nodes on Frontier!
  - 5x improvement going from 2 to 64 nodes (16 to 512 GPUs)
- Turned sequential Chapel code into multi-node, multi-GPU per node enabled code with minimal changes
- The same code runs on both NVIDIA and AMD GPUs



Looking at performance: MiniBude, BabelStream, TeaLeaf, and ChOp

# **GPU-enabled Chapel Applications and Performance**

- We discussed Coral Reef application and showed its performance on Frontier
- In the follow slides we give performance results for a few additional miniapps/applications
  - Results were copied directly from the relevant papers (with the authors' permission)
- All these run on both NVIDIA and AMD GPUs and contain no vendor-specific code

#### • BabelStream, MiniBude, and TeaLeaf

- Chapel implementations by Josh Milthorpe (Oak Ridge National Lab and Australian National University) et al.
- results are from a paper accepted for the 2024 Heterogeneity in Computing Workshop HCW (part of IPDPS)
  - "Performance Portability of the Chapel Language on Heterogeneous Architectures".
- **ChOp** (Chapel Optimization)
  - written by Tiago Carneiro (Interuniversity Microelectronics Centre (IMEC), Belgium) et al.
  - Solves N-Queens problem
  - results shown are from a submission to EuroPar (currently pending review)



# **MiniBude: Chapel implementation by Josh Milthorpe from ORNL**

- MiniBude is miniapp of Bude (a protein docking simulation)
  - The computation is very arithmetically intensive and makes significant use of trigonometric functions
- For this miniapp, Chapel's performance is close to CUDA's and HIP's
- Architectural efficiency = % of peak memory bandwidth for each platform

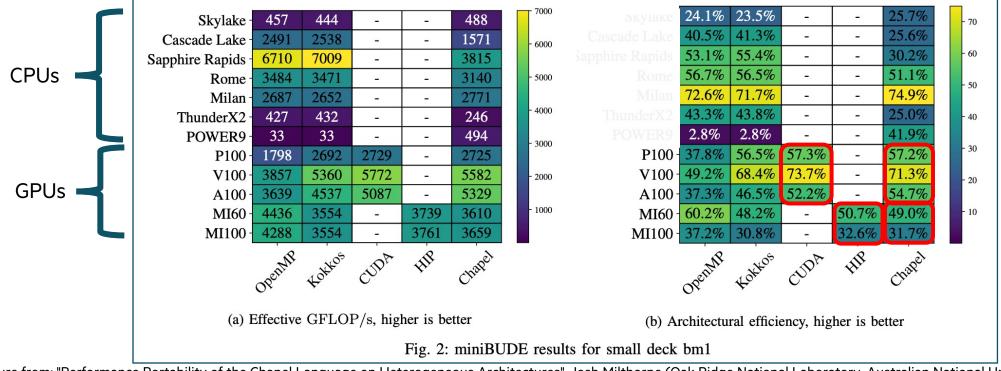


Figure from: "Performance Portability of the Chapel Language on Heterogeneous Architectures". Josh Milthorpe (Oak Ridge National Laboratory, Australian National University), Xianghao Wang (Australian National University), Ahmad Azizi (Australian National University) Heterogeneity in Computing Workshop (**HCW**)

## **BabelStream: Chapel implementation by Josh Milthorpe from ORNL**

- Performs stream triad computation computing A = B +  $\alpha$  \*C for arrays A, B, C and scalar  $\alpha$
- Chapel performs competitively for this benchmark
- Architectural efficiency = % of peak memory bandwidth for each platform

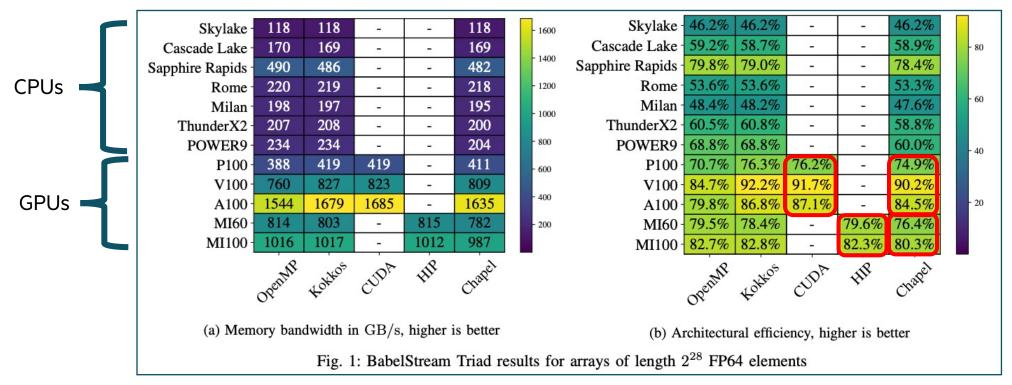


Figure from: "Performance Portability of the Chapel Language on Heterogeneous Architectures". Josh Milthorpe (Oak Ridge National Laboratory, Australian National University), Xianghao Wang (Australian National University), Ahmad Azizi (Australian National University) Heterogeneity in Computing Workshop (**HCW**)

# **Tealeaf: Chapel implementation by Josh Milthorpe from ORNL**

- Tealeaf simulates heat conduction over time
- On this application Chapel performed well on CPUs but not GPUs
  - We are investigating this and suspect better in-kernel reduction support will help close the gap
- Application efficiency = performance relative to fastest implementation for each platform

									2 <u></u>										<b>100</b>
CPUs 🗖		Skylake	491	720	-	-	466	464			Skylake -	94.4%	64.4%	-	-	99.5%	100%		100
		Cascade Lake	316	471	-	-	323	324			Cascade Lake	100%	67.1%		-	97.8%	97.7%		
	Sa	ophire Rapids	95	239	-	2-	118	116	- 4	4000	Sapphire Rapids -	100%	39.9%	-	-	80.5%	81.9%		- 80
		Rome	149	530	-	-	165	166			Rome -	100%	28.1%	-	-	90.3%	89.7%		
		Milan	246	770	-	-	269	265	- 3	- 3000	Milan -	100%	31.9%	- 1	-	91.4%	92.8%		- 60
		ThunderX2	413	864	-	-	428	432			ThunderX2 -	100%	47.8%	-	-	96.4%	95.6%		
GPUs -		POWER9	743	4781	-	-	362	361			POWER9	48.6%	7.6%	-	-	99.9%	100%		
		P100	281	247	177		2062	4133	- 2	2000	P100 -	63.2%	71.7%	100%	-	8.6%	4.3%		- 40
		V100	149	129	96	-	581	3607			V100 -	64.3%	74.3%	100%	-	16.5%	2.7%		
		A100	131	69	50	-	302	3430	- 1	1000	A100 -	38.1%	72.5%	100%	-	16.5%	1.5%		- 20
		MI60	164	115	-	251	297	4911			MI60 -	70.3%	100%	-	45.9%	38.8%	2.3%		
		MI100	148	111	-	243	231	2310			MI100 -	75.4%	100%	-			4.8%		
			OpenMP	Kokkos	CUDA	HR	Chapel	Chapel 2D				OpenMP	Pottos	CUDA	HR	Chapel	Thapel 2D		
		(a) Execution time (s), lower is better								(b) Application efficiency, higher is better									
		Fig. 3: TeaLeaf results for input tea_bm_5.in																	

Figure from: "Performance Portability of the Chapel Language on Heterogeneous Architectures". Josh Milthorpe (Oak Ridge National Laboratory, Australian National University), Xianghao Wang (Australian National University), Ahmad Azizi (Australian National University) Heterogeneity in Computing Workshop (**HCW**)

# **ChOp: N-Queens Solver by Tiago Carneiro from IMEC**

- Results are shown for two different problem sizes "21" and "22"
- The "CUDA"/"HIP" versions use Chapel's interoperability features to launch kernels written in CUDA/HIP
- For size=21 Chapel and CUDA/HIP perform similarly well, for size=22 the HIP version would crash so we don't have comparative results for that (the Chapel version would, however, scale)

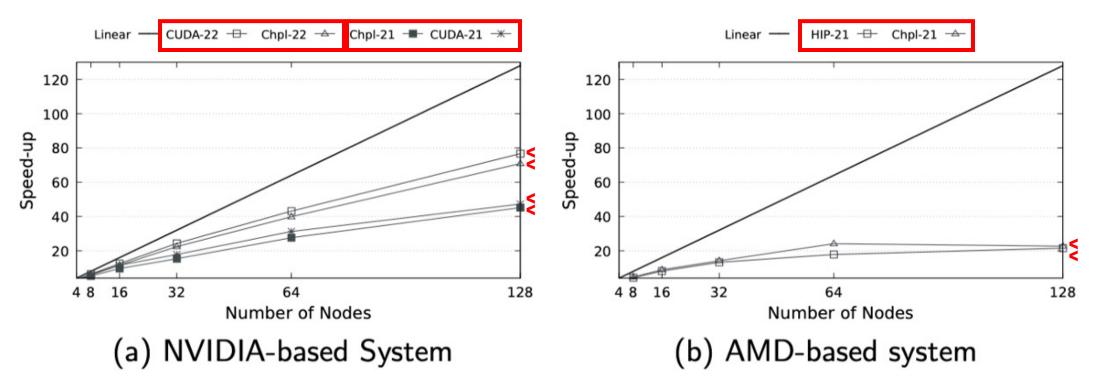
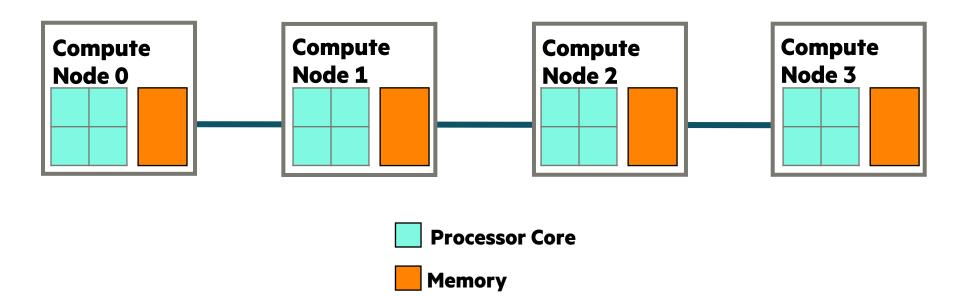


Figure from: "Investigating Portability in Chapel for Tree-Based Optimizations on GPU-powered Clusters". Tiago Carneiro, Engin Kayraklioglu, Guillaume Helbecque, Nouredine Melab

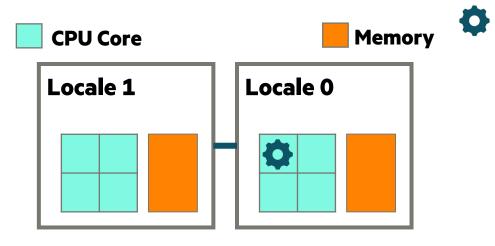
**Programming GPUs using GPU locales** 

#### **Locales in Chapel**

- Locales represent the resources of your HPC system that have:
  - processors, so it can run tasks
  - memory, so it can store variables

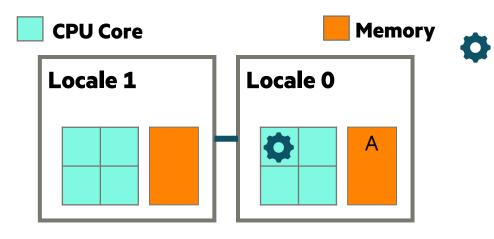


• Execution starts with a single task running on the first locale (i.e. Locale[0])



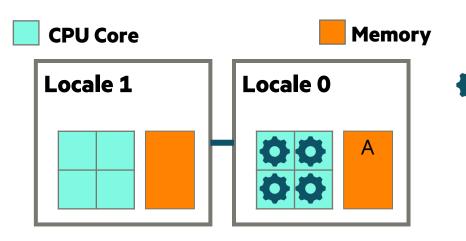
```
// Execution starts on Locale[0]
var A: [1..4] real;
coforall i in 1..4 do
    A[i] = someComputation(i);
on Locales[1] {
    var B: [1..4] real;
    B = 2;
    A = B;
}
```

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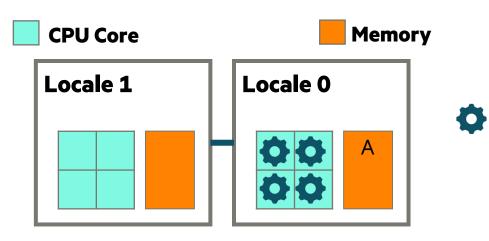
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- Use **on** statements to move an executing task from one locale to another



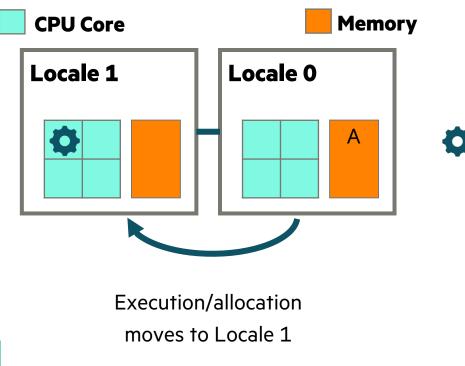
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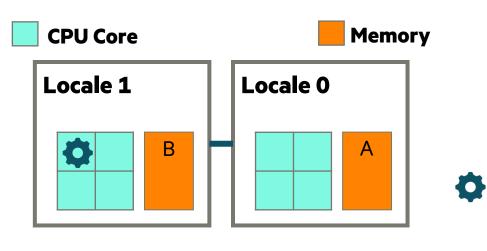
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- The Locales array contains locales for all the nodes in your system



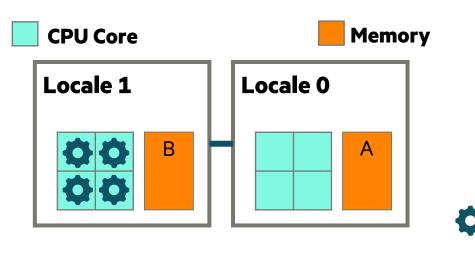
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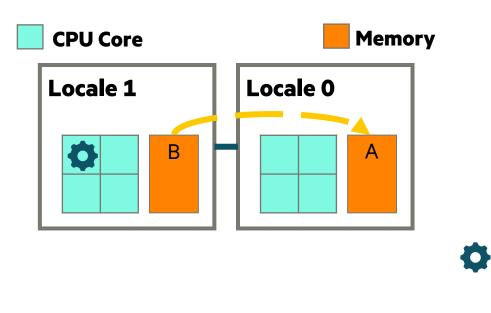
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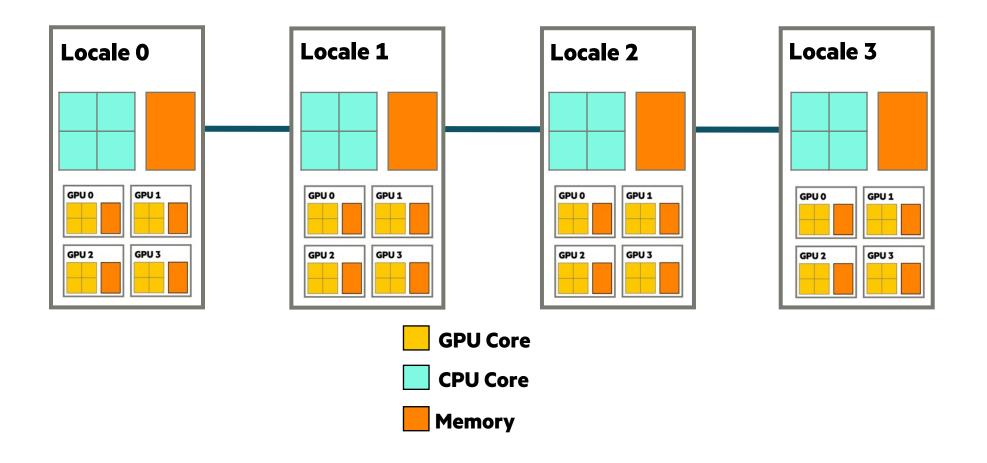
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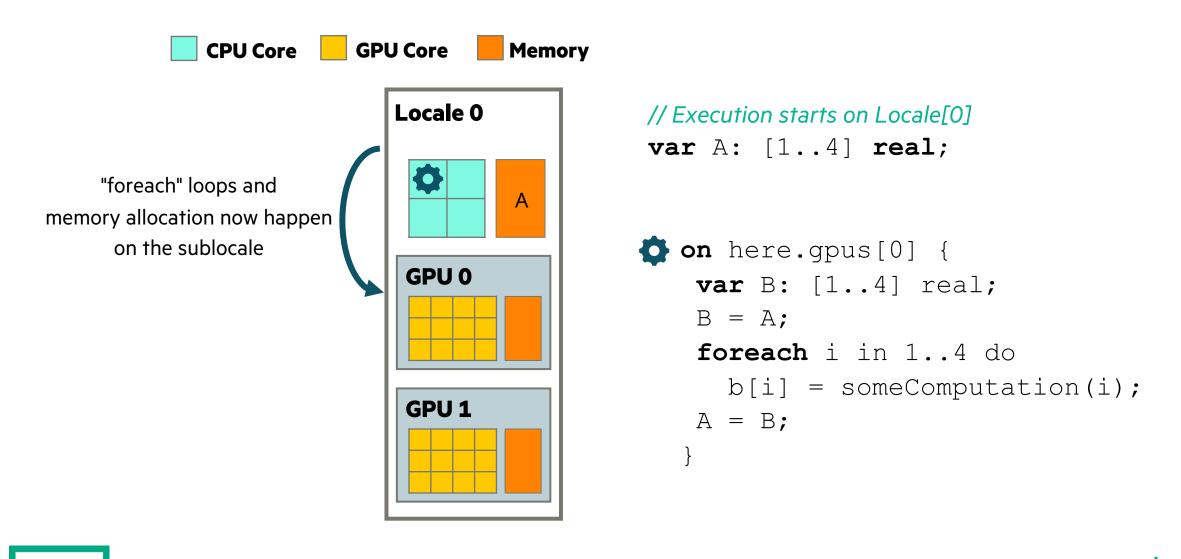
## **GPU Sublocales**

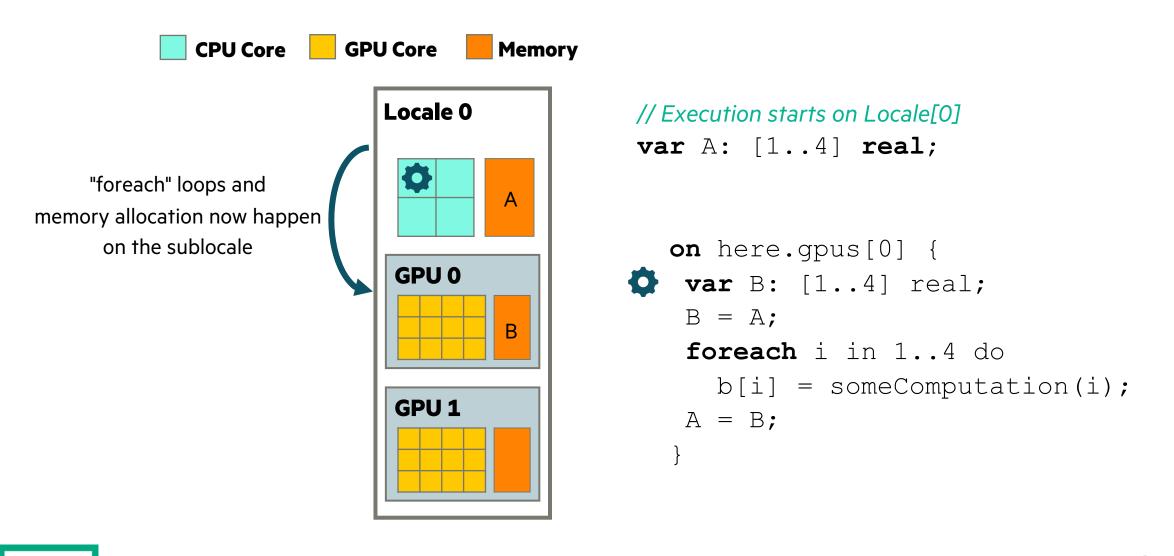
- Let's add GPU sublocales to the picture
  - These are nested under top level node locales
- Refer to gpu sublocales using the **gpus** array accessible from top-level locales

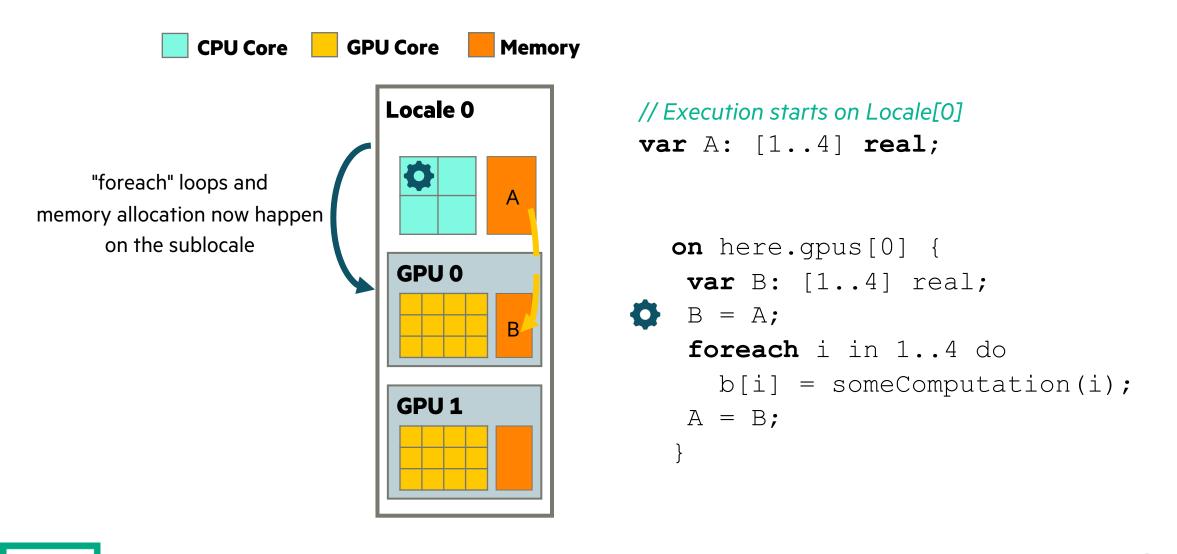


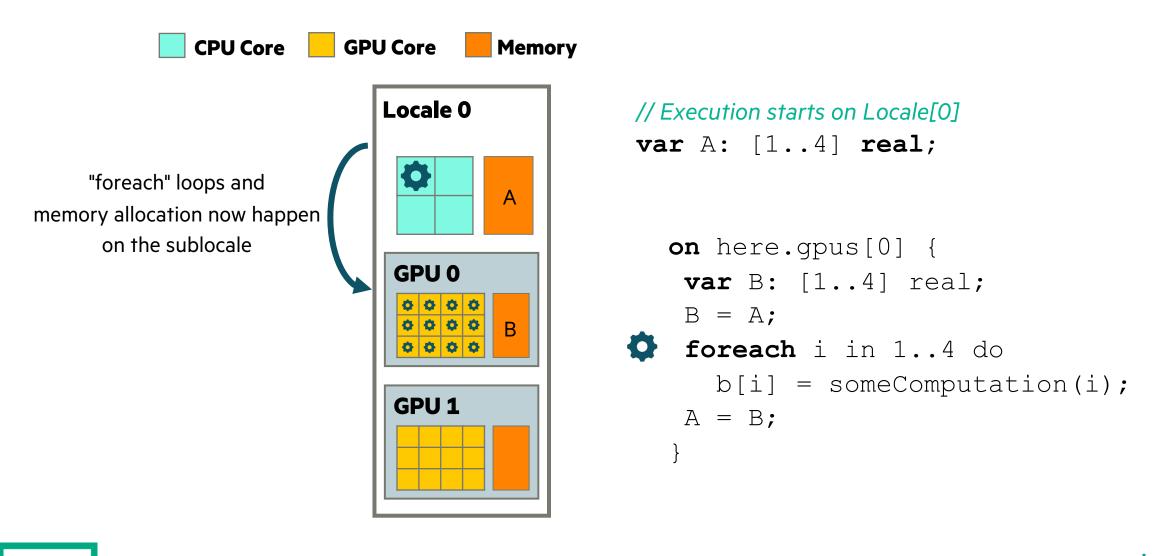
CPU Core GPU Core Memory // Execution starts on Locale[0] Locale 0 **var** A: [1..4] **real**; on here.gpus[0] { GPU 0 **var** B: [1..4] real; B = A;foreach i in 1..4 do b[i] = someComputation(i); **GPU1** A = B;

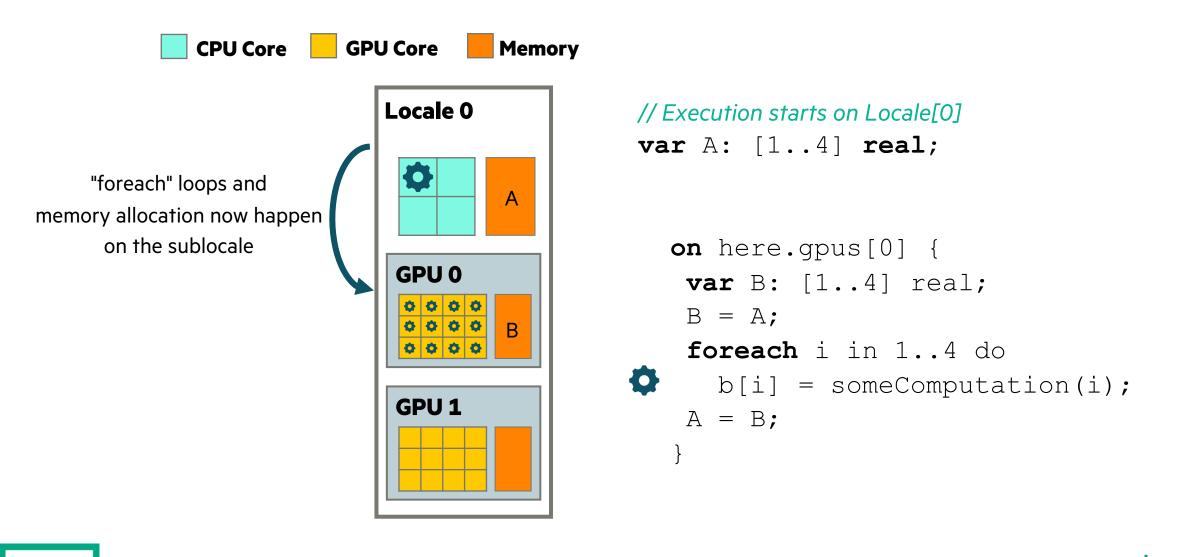
```
CPU Core GPU Core
                     Memory
             Locale 0
                              // Execution starts on Locale[0]
                            var A: [1..4] real;
               A
                                 on here.gpus[0] {
              GPU 0
                                  var B: [1..4] real;
                                  B = A;
                                  foreach i in 1..4 do
                                    b[i] = someComputation(i);
              GPU1
                                  A = B;
```

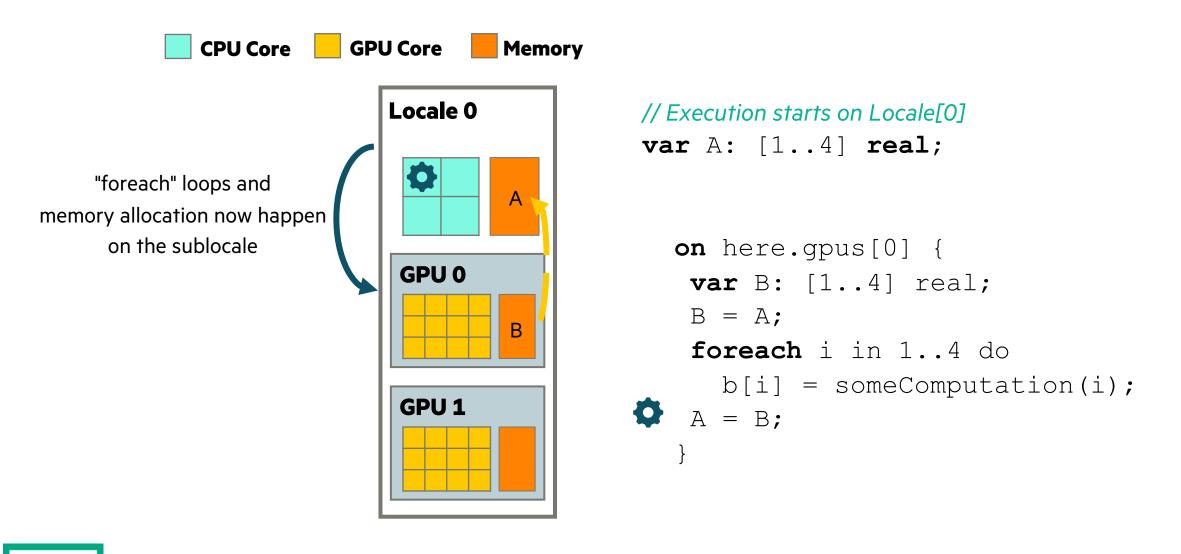


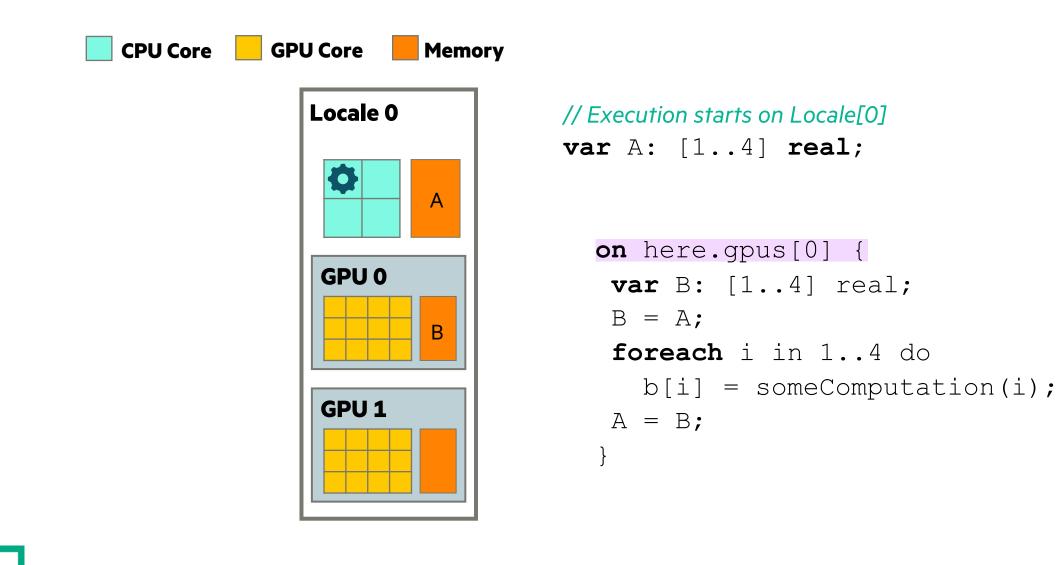


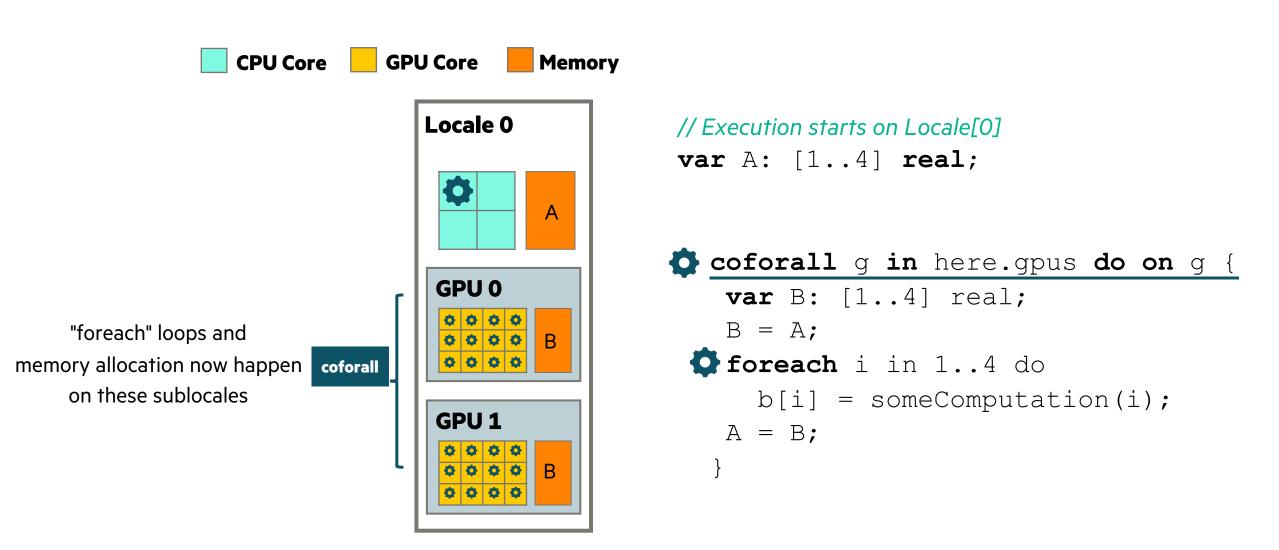


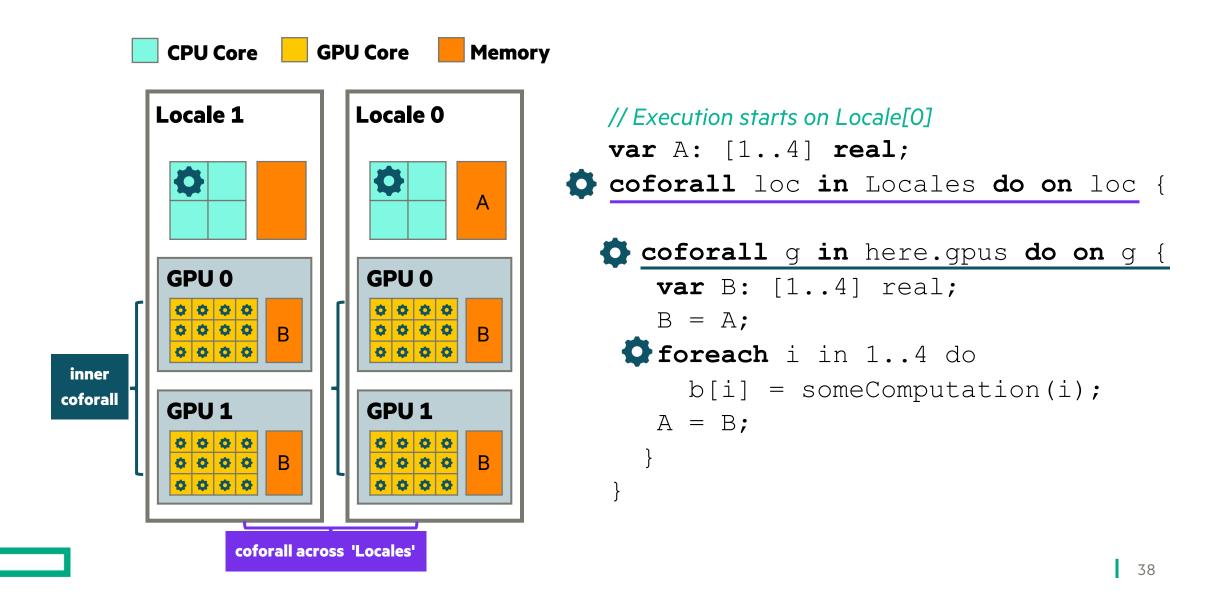












# Conclusions

#### Conclusions

- Chapel enables scalable programming programming
- Chapel represents nodes as locales and GPUs as sublocales
- We presented a handful of community-written applications
  - These work both on NVIDIA and AMD GPUs without any vendor-specific code modifications
  - Chapel performs competitively for most, we're working on Tealeaf
- GPU support in Chapel has garnered a lot of interest
- Future work includes support for Chapel's distributed arrays on GPUs and execution on Intel GPUs

# If you Want to Learn More About GPU Programming In Chapel

#### Technote: <a href="https://chapel-lang.org/docs/main/technotes/gpu.html">https://chapel-lang.org/docs/main/technotes/gpu.html</a>

- Anything and everything about our GPU support
  - configuration, advanced features, links to some tests, caveats/limitations
- More of a reference manual than a tutorial

#### Blogpost: <a href="https://chapel-lang.org/blog/posts/intro-to-gpus/">https://chapel-lang.org/blog/posts/intro-to-gpus/</a>

• Tutorial on GPU programming in Chapel

#### **Previous talks**

- CHIUW '23 Talk: updates from May '22-May '23 period
  - https://chapel-lang.org/CHIUW/2023/KayrakliogluSlides.pdf
- LCPC '22 Talk: a lot of details on how the Chapel compiler works to create GPU kernels
  - https://chapel-lang.org/presentations/Engin-SIAM-PP22-GPU-static.pdf
- Recent Release Notes: almost everything that happened in each release
  - https://chapel-lang.org/release-notes-archives.html



Chapel is a programming language for productive parallel computing. In recent years, a particular subdomain of parallel computing has exploded in popularity: GPU computing. As a result, the Chapel team has been hard at work adding GPU support, making it easy to create vendor-

#### **Chapel Resources**

#### Chapel homepage: <u>https://chapel-lang.org</u>

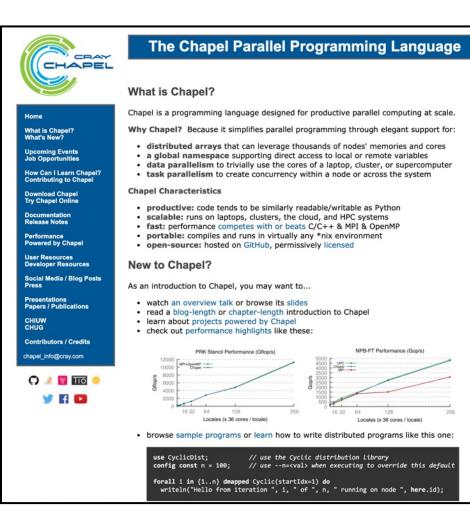
• (points to all other resources)

#### Social Media:

- Twitter: <u>@ChapelLanguage</u>
- Facebook: <a>@ChapelLanguage</a>
- YouTube: <a href="https://www.youtube.com/c/ChapelParallelProgrammingLanguage">https://www.youtube.com/c/ChapelParallelProgrammingLanguage</a>
- Blog: <u>https://chapel-lang.org/blog/</u>

#### **Community Discussion / Support:**

- Discourse: <a href="https://chapel.discourse.group/">https://chapel.discourse.group/</a>
- Gitter: https://gitter.im/chapel-lang/chapel
- Stack Overflow: <a href="https://stackoverflow.com/questions/tagged/chapel">https://stackoverflow.com/questions/tagged/chapel</a>
- GitHub Issues: <a href="https://github.com/chapel-lang/chapel/issues">https://github.com/chapel-lang/chapel/issues</a>



#### **HPE Developer Meetup**

# Meetup for "Vendor-Neutral GPU Programming in Chapel"

Jul 31, 2024 08:00 AM PDT (-7 UTC)

Jade Abraham, Engin Kayraklioglu



speakers will discuss Chapel's GPU support in detail and collaborate with you to determine how it may help in your particular situation.

HPE developer meetups home page:

https://developer.hpe.com/campaign/meetups/



**Registration:** 

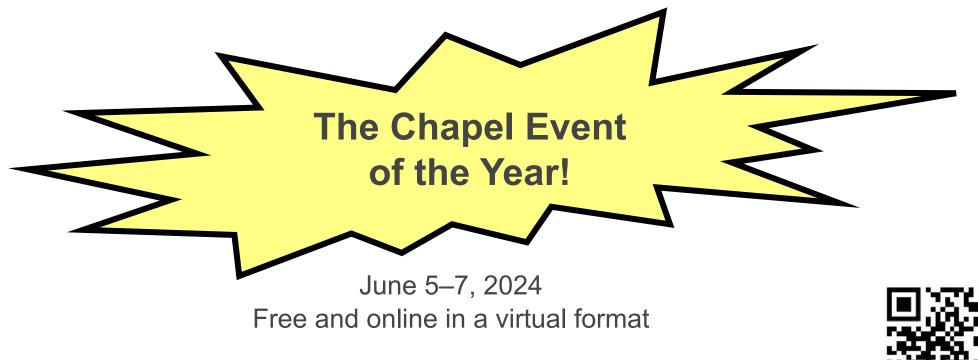
https://hpe.zoom.us/webinar/register/3117139444656/WN\_ojVy9LR\_QHSCGxeg21rj7A



#### ChapelCon

# ChapelCon

https://chapel-lang.org/ChapelCon24.html



Tutorials, open lab sessions, demos, and talks





#### Conclusions

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- Chapel represents nodes as locales and GPUs as sublocales
- We presented a handful of community-written applications
  - These work both on NVIDIA and AMD GPUs without any vendor-specific code modifications
  - Chapel performs competitively for most, we're working on Tealeaf
- GPU support in Chapel has garnered a lot of interest
- Future work includes support for Chapel's distributed arrays on GPUs and execution on Intel GPUs

